## REPLY UNDER 37 CFR 1.116 EXPEDITED PROCEDURE TECHNOLOGY CENTER 2800

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.

10/711,959

Conf. No.

5958

In Re Application of:

Heng et al.

Art Unit:

2825

Filed:

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Dkt. #:

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(IBMB-0059)

Title: INTEGRATED CIRCUIT

SELECTIVE SCALING

Examiner:

Kik, Phallaka

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AFTER-FINAL AMENDMENT

Sir:

## I. INTRODUCTORY COMMENTS

In response to the Final Office Action of January 05, 2007, please amend the abovereferenced patent application as follows: